

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: CHAKIHARA, et al.
Serial No.: Not yet assigned
Filed: July 30, 2003
For: A SEMICONDUCTOR MEMORY DEVICE AND A METHOD OF
MANUFACTURING THE SAME, A METHOD OF
MANUFACTURING A VERTICAL MISFET AND A VERTICAL
MISFET, AND A METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR
DEVICE
Group: Not yet assigned
Examiner: Not yet assigned

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 30, 2003

Sir:

The following amendments and remarks are respectfully submitted in
connection with the above-identified application.